

INVENTOR NAME: Tat Hin Tan SERIAL NO.: 10/751,003

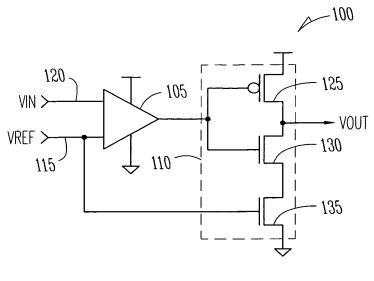


Fig. 1A

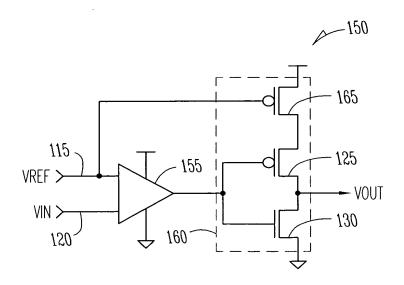


Fig. 1B

INVENTOR NAME: Tat Hin Tan SERIAL NO.: 10/751,003

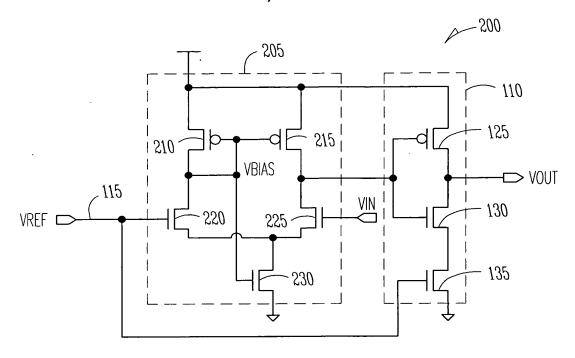


Fig. 2A

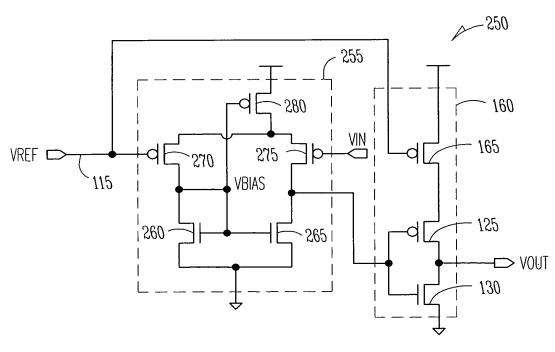


Fig.2B

TITLE: OFFSET-COMPENSATED SELF-BIASED DIFFERENTIAL AMPLIFIER INVENTOR NAME: Tot Hin Ton

SERIAL NO.: 10/751,003

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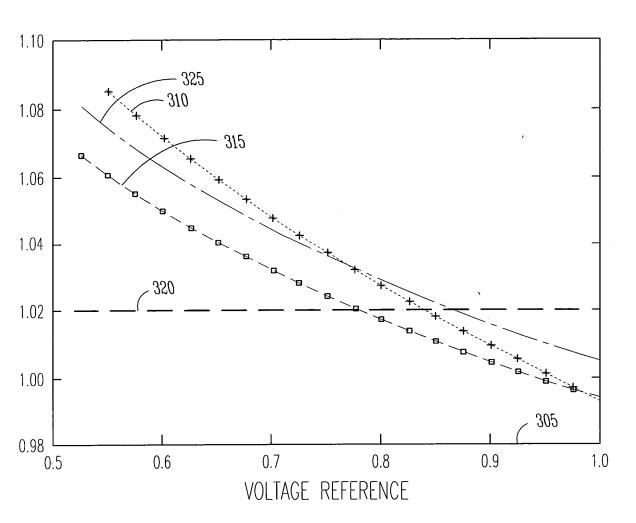


Fig. 3

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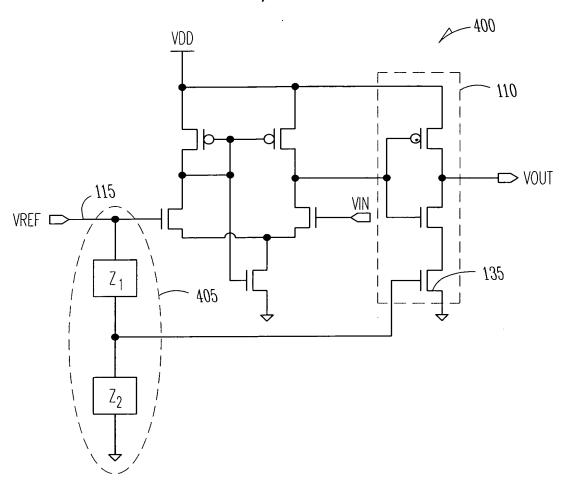


Fig. 4A

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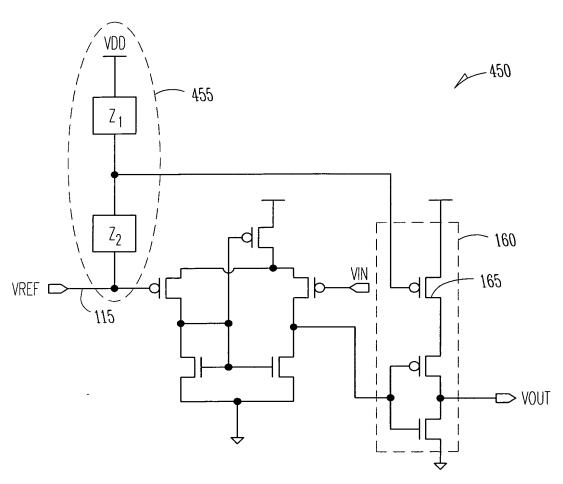
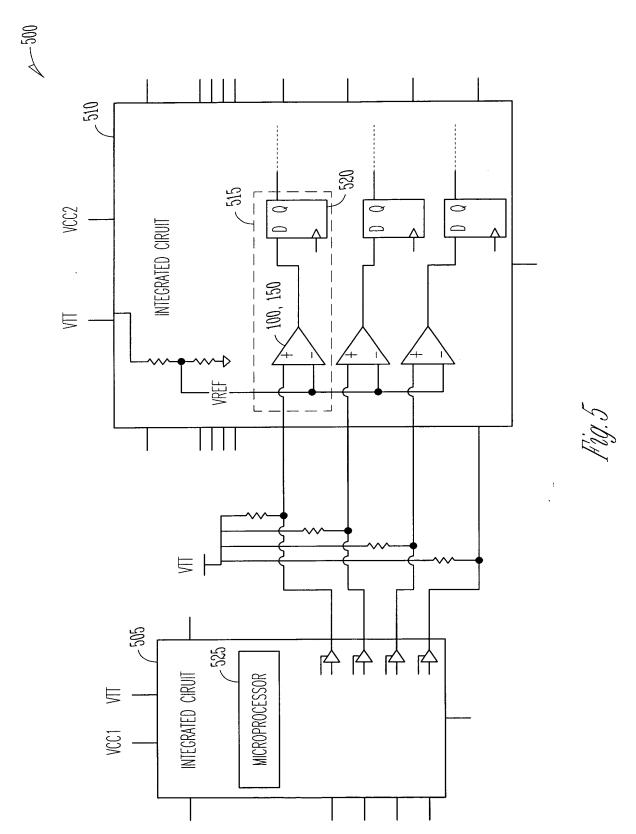


Fig. 4B

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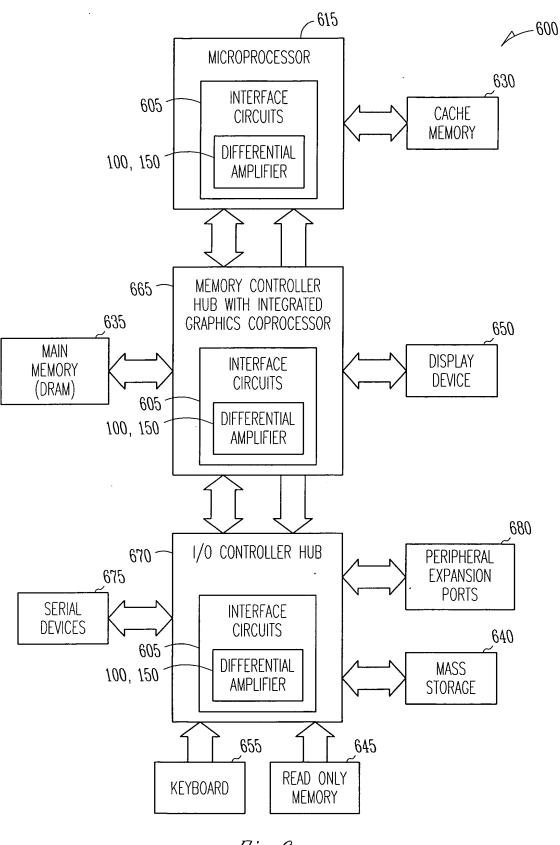


Fig. 6

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~700 710 APPLYING A REFERENCE SIGNAL VOLTAGE TO A FIRST INPUT OF A DIFFERENTIAL INPUT AMPLIFIER, THE DIFFERENTIAL AMPLIFIER HAVING A COMMON MODE BIAS VOLTAGE AND THE OUTPUT OF THE DIFFERENTIAL AMPLIFIER COUPLED TO AN INPUT OF AN INVERTER 750 APPLYING AN INPUT SIGNAL TO A SECOND INPUT OF A DIFFERENTIAL AMPLIFIER, THE INPUT SIGNAL CAUSING THE DIFFERENTIAL AMPLIFIER TO SWITCH OUTPUT STATES AS AN INPUT SIGNAL VOLTAGE APPROACHES THE REFERENCE SIGNAL VOLTAGE 730 ADJUSTING THE SWITCHING POINT OF THE INVERTER TO TRACK THE COMMON MODE BIAS VOLTAGE OF THE DIFFERENTIAL AMPLIFIER BY COUPLING THE REFERENCE SIGNAL VOLTAGE TO A GATE REGION OF A TAIL TRANSISTOR CONNECTED IN SERIES BETWEEN THE INVERTER AND A POWER SUPPLY RAIL

Fig. 7